## FOURTH YEAR ELECTRONICS AND COMPUTER ENGINEERING PROGRAM SYLLABUS, REVISED COURSE (2019-2020)

<u>SEMESTER – VII</u>			
Name of the Prog	gramme	: Electronics and Computer Engineering	
Course Code		: ECOMP710	
Title of the Course		: Industrial Automation and Instrumentation	
Number of Credit	ts	:03	
Effective from AY		: 2024-25	
Pre-requisites	Basic Elect	ronics	
for the Course:			
Course Objectives:	<ol> <li>The subject aims to provide the student with:</li> <li>An understanding of the principle and working of the Data acquisition systems, CRO and different types of transducers.</li> <li>Introduction to Virtual Instrumentation using LABVIEW.</li> <li>Introduction to the automation systems using the programmable logic controllers.</li> <li>An understanding of the different types of industrial interfacing standards.</li> </ol>		quisition mmable erfacing
AND	Upon cor	npletion of the course, students will be able to	2
	ECOMP 710.1	Explain the principle and working of the CRO, DSG acquisition systems, different types of in communication standards.	), Data dustrial
Course Outcomes:	ECOMP 710.2	Design and simulate virtual instruments for sensors a acquisition using appropriate software.	nd data
Conconness.	ECOMP 710.3	Evaluate between different types of transducers for application.	a given
	ECOMP 710.4	Design and simulate various industrial control appl using the programmable logic controllers.	ications
	UNIT- 1		
Content:	Electronic Voltmeter: Non-integrating type: Ramp type, Staircase Ramp, Continuous balance, Successive Approximation. Integrating type: Potentiometer Integrating, Dual Slope Integrating Voltmeter. Sensitivity & Resolution of a DVM. Oscilloscope: Block diagram, Classification of CROs, CRT control circuits, delay lines, multiple trace CRO, Time base circuits, synchronizing circuits, Digital storage oscilloscope. CRO probes: Active & Passive probes, Compensation for probes. Virtual Instrumentation: Historical perspective, advantages, block diagram, Virtual instruments examples, the front panel, Sub VIs. Data Acquisition systems (DAS): Basic block diagram of DAS, objective of DAS. Components of a DAO system types of signals		11 Hours.

	signal conditioning of inputs, importance of instrumentation and isolation amplifier, Signal grounding and measurements, DAQ hardware configuration, Digital and Analog I/O considerations.	
	UNIT-2	
	TemperatureMeasurementTransducers:ResistanceTemperature Detectors, Thermistors, Thermocouples.DisplacementTransducers:Basicdifferenttypesofdisplacementtransducers:StrainGauge, LinearVariableDifferentialTransformer, Capacitive,Inductive, Piezoelectric, and Potentiometer.PressurePressureTransducers:Inductive, Resistiveand Capacitivetransducers for measuring pressure.VelocityVelocityTransducers:Basicprincipleofmeasurementtransducers:TurbomagneticFlowmeasurementtransducers:Photoresistor,Photodiode,Distatransistor	11 Hours
AUNVER .	Phototransistor.	ERE
	UNIT-3	All C
	<ul> <li>Programmable Logic Controllers (PLC): PLC Advantages &amp; Disadvantages, Overall PLC System, CPU &amp; Programmable Monitors, PLC input &amp; Output Modules (Interfaces).</li> <li>General PLC Programming Procedure: Proper Construction of PLC Ladder diagrams, Process Scanning considerations.</li> <li>Basic PLC Programming: Programming ON-OFF inputs to produce ON-OFF outputs, Concepts of latching, interlocking, jogging outputs via ladder programming.</li> <li>PLC Timer Functions: PLC timer functions, Examples of timers and Industrial process timing applications.</li> <li>PLC Counter functions: PLC Counters, Examples of Counter Functions, Industrial applications.</li> <li>PLC data handling instructions: Move, Conditional Jump, Call Subroutine instructions. Selecting a PLC: Factors to be considered while selecting a PLC.</li> <li>UNIT -4</li> </ul>	11 Hours.
	SCADA systems: Introduction and brief history of SCADA	
	Modern SCADA systems, SCADA software, Remote terminal units. Data logger basics, Advantages of data loggers, anatomy of a data logger, types of data loggers, factors to be considered in selecting a datalogger. <b>Basic standards</b> : RS-232 and RS-485, Electrical signal characteristics, Modbus: General overview, Modbus protocol structure.	12 Hours.

	Fieldbus: Introduction, General Fieldbus architecture, Basic requirements of Fieldbus standard.			
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constructive learning and Collaborative learning. One or more assignments to be carried out on topics covered in each unit above- Total time allotted 15 hours.			
References/ Readings:	<ol> <li>Text Books:         <ol> <li>H. S. Kalsi; Electronic Instrumentation; 3E. (2012). India: Tata McGraw-Hill.</li> <li>Robert H. Bishop; Learning with LABVIEW 7 Express; Pearson Education.</li> <li>John Webb, Ronal Weiss; Programmable Logic Controllers: Principles &amp; Applications, 5th Edition; Prentice Hall of India.</li> </ol> </li> <li>Reference book         <ol> <li>Deon Reynders , Steve Mackay , Edwin Wright; Practical Industrial</li> </ol> </li> </ol>			
	<ul> <li>Data Communications: Best Practice Techniques; Newnes , 2004, An imprint of Elsevier.</li> <li>Clarke, G., Reynders, D., Wright, E.; Practical Modern SCADA Protocols DNP3, 60870.5 and Related Systems, 1st Edition, Newnes, An imprint of Elsevier</li> </ul>			









Name of the Prog	gramme	: Electronics and Computer Engineering	
Course Code		: ECOMP721	
Title of the Course		: Blockchain Technology	
Number of Credit	Credits : 03		
Effective from AY	/	: 2024-25	
Pre-requisites	Expertise	in programming, basic knowledge of computer security,	
for the Course:	cryptogra	phy.	
Course Objectives:	<ol> <li>The subject aims to provide the student with:</li> <li>To understand the building blocks of Blockchain.</li> <li>To understand significance Distributed Ledger Technology and Sma Contract.</li> <li>To study the impact of security in Blockchain.</li> <li>To exploit applications of Blockchain in real world scenarios and th impacts.</li> </ol>		Smart I their
	Upon co	mpletion of the course, students will be able to	
	ECOMP 721.1	Understand Blockchain ecosystem and its services in world sceneries	n real
Course Outcomes:	ECOMP 721.2	Apply and Analyze the requirement of Distributed L Technology and Smart Contract.	edger
	ECOMP 721.3	Design and Demonstrate end-to-end decentrapplications.	alized
	ECOMP 721.4	Acquaint the protocol and assess their computa requirements.	tional
िविश्वाय	UNIT-1		D
Content:	Fundame Blockchai in a Block Blockchai Mechanis of Blockch	ntalsofBlockchain:Introduction,Originofn,BlockchainSolution,ComponentsofBlockchain,Blockchain,The technology and the future.nTypesandConsensusm:Introduction,DecentralizationandDistribution,Typesnain,ConsensusProtocol	11 Hours
	UNIT-2	A CALLER CONTRACTOR	
	Cryptocurrency – Bitcoin, Altcoin and Token: Introduction, Bitcoin and the Cryptocurrency, Cryptocurrency Basics, Types of Cryptocurrency, Cryptocurrency Usage Public Blockchain System: Introduction, Public Blockchain, He Popular Public Blockchains, The Bitcoin Blockchain, Ethereum Blockchain		11 Hours
	UNIT -3		
	Smart Co a Smart Oracles,Si Private B	ntracts: Introduction, Smart Contract, Characteristics of Contract, Types of Smart Contracts,Types of mart Contracts in Ethereum. Iockchain System: Introduction, Key Characteristics of	11 Hours

	Private Blockchain, Why We Need Private Blockchain, Private Blockchain Examples, Private Blockchain and Open Source, Smart Contract in Private Environment, State Machine, Different Algorithms of Permissioned Blockchain, Byzantine Fault, Multichain	
	UNIT -4	
	Consortium Blockchain: Introduction, Key Characteristics of Consortium Blockchain, Why We Need Consortium Blockchain, Hyperledger Platform. Security in Blockchain: Security Aspects in Bitcoin, Security and Privacy Challenges of Blockchain in General, Performance and Scalability, Identity Management and Authentication Limitations and Challenges of Blockchain: Blockchain Implementation – Limitations, Challenges.	12 Hours
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constructi learning and Collaborative learning	ve
References/ Readings:	<ol> <li>Text Books:         <ol> <li>Chandramouli Subramanian, Asha A George , Abhilash K A , Karthikeyan, Blockchain Technology, Universities Press (Indi Ltd., 1<sup>st</sup> Edition Publication,2020H. S. Kalsi; Ele Instrumentation; Tata McGraw Hill.</li> </ol> </li> <li>Reference book         <ol> <li>Dhillon, V., Metcalf, D., and Hooper, M, Blockchain e applications, 2017, 1st Edition, CA: Apress, Berkeley.</li> <li>Wattenhofer, R. P, Distributed Ledger Technology: The Science Blockchain (Inverted Forest Publishing), 2017, 2nd E Createspace Independent Pub, Scotts Valley, California, US.Ne An imprint of Elsevier.</li> </ol></li></ol>	Meena a) Pvt. cctronic nabled e of the Edition, ewnes ,





Name of the Prog	gramme	: Electronics and Computer Engineering	
Course Code		: ECOMP722	
Title of the Cours	e	: Machine Learning	
Number of Credits		: 03	
Effective from AY		: 2024-25	
Pre-requisites	Engineeri	Engineering Mathematics	
for the Course:		AND	
Course Objectives:	The subj 1. The b assoc learni 2. Profic and ro 3. Famil 4. Explo dimen	ect aims to provide the student with: basic understanding of machine learning, covering concepts like iation learning, classification, regression, and reinforcemening. sciency in supervised learning techniques such as classification egression techniques iarization of clustering techniques ration of advanced concepts such as outlier detection insionality reduction and model selection and evaluation.	
OS UNVERSION	Upon co ECOMP 722.1	mpletion of the course, students will be able to: Explain fundamental machine learning concepts and select appropriate machine learning algorithms; perform classification tasks by implementing basic and advanced classification methods.	
Course	ECOMP 722.2	Evaluate the classifier performance using appropriate metrics, and employing techniques to enhance classification accuracy	
Outcomes:	ECOMP 722.3	Apply cluster analysis techniques to identify meaningful patterns in data, selecting suitable algorithms for different types of data.	
	ECOMP 722.4	Apply dimensionality reduction techniques to simplify complex datasets and outlier detection methods to identify anomalous data points, thus improving the quality and efficiency of machine learning models.	
	UNIT- I	nowledge is Divine	
Content:	Introduct Applicatio Under fit Trade-off Getting t Statistica Measures Integratio Transforr Basic Clas Bayesian	<ul> <li>What Is Machine Learning? Examples of ML ons, ML Basics: Learning Algorithm, Capacity, Overfitting, ting, Hyper parameters and validation sets, Bias-Variance</li> <li>Ko Know Data: Data Objects and Attribute Types, Basic I Descriptions of Data, Data Similarity and Dissimilarity and Dissimilarity and Correlation Analysis, Data nation by Normalization</li> <li>Sification Methods: Decision Tree Induction (ID3), Naïve Classification, Rule-Based Classification (IF-THEN rules,</li> </ul>	
	rules extr	action from decision tree)	

	UNIT-2	
	Model Evaluation and Selection: Metrics for Evaluating Classifier Performance, Holdout Method and Random Subsampling, Cross- Validation, Bootstrap, ROC Curves, Techniques to Improve Classification Accuracy, Random Forests Advanced Classification Methods: Support Vector Machines, Classification Using Frequent Patterns, Lazy Learners: K- Nearest Neighbour	11 Hours
	UNIT-3	
	<b>Cluster Analysis:</b> Requirements, Partitioning Methods: k-means & k- medoids, Hierarchical Methods: Agglomerative versus Divisive Hierarchical Clustering, Distance Measures in Algorithmic Methods, BIRCH, Density-Based Methods: DBSCAN, Probabilistic Model-Based Clusters, Fuzzy Clusters, Expectation-Maximization Algorithm	11 Hours
	UNIT-4	
	Regression: Linear (simple & multiple), Logistic Regression Outlier Analysis: Types of Outliers, Challenges of Outlier Detection, Outlier Detection Methods, Parametric Methods Dimensionality Reduction: Introduction, Subset Selection, Principal Components Analysis, Linear Discriminant Analysis	11 Hours
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constr learning and Collaborative learning	ructive
References/	Text Books:	
Readings:	<ol> <li>Jiawei Han and Micheline Kambers and JianPei, "Data Mining Con and Techniques", 3<sup>rd</sup> Edition, Morgan Kaufman Publications, 2012.</li> <li>Ethem Alpaydin, "Introduction to Machine Learning", MIT Press, Pr Hall of India, 4<sup>th</sup> Edition 2020.</li> <li>Ian Goodfellow, Yoshua Bengio and Aaron Courville "Deep Learning Press book, 2016.</li> </ol>	ncepts rentice g "MIT
	Reference book	
	<ol> <li>Kevin P. Murphy, "Machine Learning: A Probabilistic Perspective MIT Press, 2012.</li> </ol>	", The
	2. Daniel T. Larose, "Data mining and predictive analytics", WILEY, S Edition, 2015	econd



Name of the Prog Course Code Title of the Cours Number of Credit	gramme e ss	: Electronics and Computer Engineering : ECOMP723 : Hardware Description Languages : 03 : 2024 25	
	<b>D:</b> :: 10	: 2024-23	
Pre-requisites	Digital Sy	stem Design	
for the Course:			
Course Objectives:	<ol> <li>An understanding of the syntax and semantics of Verilog HDL.</li> <li>An ability to write Verilog programs for combinational circuits.</li> <li>An ability to write Verilog programs for sequential circuits.</li> <li>Knowledge about syntax and semantics of System Verilog.</li> </ol>		
	Upon co	mpletion of the course, students will be able to	
	ECOM P723.1 ECOM	<b>Explain</b> the syntax and semantics of Verilog HDL. <b>Write</b> programs to design combinational circuits using V	/erilog
Course	P723.2	HDL	0
Outcomes:	ECOM	Write programs to design Sequential circuits using Veril	og
0-0	P723.3	HDL.	
O OF UNIVERSION	ECOM	<b>Explain</b> the syntax and semantics of System Verilog.	
Smark	P723.4		275
9 6000	UNIT-1		2211
Content:	Emergence of HDLs, Design Flow using HDLs, Importance of HDLs. Hierarchical Modeling Concepts: Modules, Instances. Data Types: Nets, Registers, Vectors, Integer, Real, and Time, Arrays, Memories, Parameters, Strings. Modules and Ports. Gate Level Modeling: Design of Ripple Carry Adder, Multiplexer, Demultiplexer, Decoder, Encoder. Test benches to verify the Functionality.		
	Dataflow	Modeling: Continuous assignment (assign)	
	statement Delays: I and net c Define ex Operator relationa Replication Examples Full Adde UNIT -3	Regular assignment delay, implicit assignment delay, leclaration delay. pressions, operators, and operands. <b>types:</b> for all possible operations - arithmetic, logical, l, equality, bitwise, reduction, shift, concatenation, on and conditional. <b>s:</b> 4:1 Multiplexer, Demultiplexer, Decoder, Encoder, r, Test benches to verify the Functionality.	11 Hours
	Behavior	al Modeling:	
	Structure	d procedures: always and initial. Blocking and non-	11
	blocking	procedural assignments. Conditional statements using	Hours

	if and else. Multiway branching using case, casex, and casez			
	statements, looping statements using while, for, repeat, and			
	forever. Definition of sequential and parallel blocks. Generate			
	Blocks (Loop, Conditional, Case)			
	Examples: 4:1 Multiplexer, Flip Flops, shift registers, counters,			
	ALU. Test benches to verify the Functionality.			
	UNIT -4			
	Tasks and functions: Differences between Tasks and Functions			
	in Verilog, Task and Function declaration and invocation.			
	Examples.			
	Finite State Machine: Examples of design using Verilog HDL.			
	Switch Level Modeling: Switch Level Elements- MOS, CMOS,			
	Bidirectional and Resistive Switches, Power and Ground.	12		
	Examples- CMOS Inverter, NAND, NOR, 2:1 Multiplexer.			
	System Verilog: Introduction to System Verilog, Difference			
	Between System Verilog and Verilog.			
	Basic Constructs of System Verilog: Data types, Operators,			
	Arrays, Queues, Associative Arrays, Common array manipulation			
	methods, printing arrays.			
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Cons learning and Collaborative learning	structive		
	Text Books:	SAC		
6 48	1 Samir Palnitkar "Verilog HDL: A Guide to Digital Desi	on and		
	Synthesis" Pearson Second Edition			
SIERIA	2. J. Bhasker, "Verilog HDL Synthesis - A Practical Primer". Sta	Galaxy		
References/	Publishing, Allentown, PA) 1998.	18/		
Readings:	Reference book	3		
	1. "IEEE std 1364-95, Verilog Language Reference Manual", IEEE I	Press		
	(NY, USA), 1995.			
	2. Stuart Sutherland and others, "System Verilog for Design" Sprir			
	Second Edition			



Name of the Programme	: Electronics and Computer Engineering
Course Code	: ECOMP724
Title of the Course	: Wireless Sensor Networks
Number of Credits	: 03
Effective from AY	: 2024-25

Pre-requisites	Nil		
for the Course:			
	1. An introduction to wireless sensor network architecture, Se and applications.	oftware	
Course	<ol> <li>An understanding of protocol stack used in wireless sens networks.</li> </ol>		
Objectives:	3. An understanding of MAC protocols and routing algorithms.		
	<ol> <li>An understanding of different strategies used in management, time synchronization protocols, loca techniques and security challenges.</li> </ol>	power llization	
	Upon completion of the course, students will be able to		
	ECOMP Describe the hardware – software aspects	and	
CONTROL OF	ECOMP Analyze and evaluate the performance of difference o	erent	
Outcomes:	ECOMPAnalyze and evaluate the performance of diffe724.3WSN routing strategies.	rent	
SER	ECOMPAnalyze various time synchronization protocols in724.4WSN.	B	
Constant and	ECOMPEvaluatevariouspowermanagementstrate724.5localization techniques in WSN.	gies,	
	UNIT-1		
Content	Introduction and Overview of Wireless Sensor Networks (WSN): Basic terminology – Sensing & Sensors, WSN, Challenges and Constraints, Protocol Stack for Wireless Sensor Networks. Applications of WSN with prototypes: Structural Health Monitoring - sensing seismic events, Traffic Control, Health Care, Pipeline Monitoring, Precision Agriculture. Node Architecture: Sensing subsystem, processor subsystem, communication interfaces, Prototype – IMote Node Architecture.	10 hours	
	Madium accors control: Overview Mirelass MAC protocols		
	Characteristics of MAC protocols in sensor networks.		
	Contention-free MAC protocols: Characteristics, traffic-	10	
	adaptive medium access, Y-MAC, Low-Energy Adaptive	hours	
	Clustering Hierarchy.	nouis	
	<b>Contention-based MAC protocols:</b> Power aware multi-access with signalling, Sensor MAC, Timeout MAC, Pattern MAC.		

	Routine enhanced MAC, Data Gathering MAC.	
	UNIT-3	
	<ul> <li>Network Layer: Introduction, Routing Metrics, Flooding and Gossiping, data Centric Routing Algorithms, Proactive Routing Algorithms, On-demand Routing Algorithms, Hierarchical Routing, Location based Routing Algorithms and QoS based Routing.</li> <li>Time synchronization: The synchronization problem, Reasons and challenges in time synchronization, Basics of time synchronization, Time Synchronization Protocols: Reference Broadcasts using Global Sources, Lightweight Tree Based Synchronization Protocol; Reference Broadcast Synchronization, Time Diffusion Synchronization Protocol.</li> </ul>	13 hours
	UNIT-4	
	<ul> <li>Power Management: Local Power Management Aspects, Dynamic Power Management.</li> <li>Localization: Introduction, ranging techniques: ToA, TDoA, AoA, RSSI. Range based localization, GPS-Based Localization, Range-free localization, Event driven localization.</li> <li>Security in WSN: Fundamentals, Challenges of security, Security attacks in sensor networks, protocols and mechanisms for security.</li> <li>Inquiry based learning. Integrative. Reflective Learning . Construction</li> </ul>	12 hours
Pedagogy:	learning and Collaborative learning	ES-
References/ Readings:	<ul> <li>Textbooks:</li> <li>1. Fundamentals of Wireless Sensor Networks: Theory and Pract Waltenegus W. Dargie; Christian Poellabauer; John Wiley &amp; Ltd., 2010.</li> <li>References:</li> <li>1. Wireless Sensor Networks: Technology, Protocols and Applic by Taieb Znati Kazem Sohraby, Daniel Minoli, John Wiley &amp; Ltd., 2007.</li> <li>2. Wireless Sensor Networks: An Information Processing Apply by Feng Zhao, Leonidas J. Guibas, Morgan Kaufmann Publ 2005.</li> <li>3. Wireless Sensor Networks: A Networking Perspective, Jun Abbas Jamalipour, Wiley, 2009.</li> <li>4. Introduction to Wireless Sensor Networks, Anna Förster, Wile Press; John Wiley &amp; Sons, 2016</li> </ul>	

Name of the Prog	gramme : Electronics and Computer Engineering		
Course Code	: ECOMP725		
Title of the Cours	rse : Advanced Database Systems		
Number of Credit	dits : 03		
Effective from AY	: 2024-25		
Pre-requisites	DBMS		
for the Course:	CUN C		
Course Objectives:	<ol> <li>The subject aims to provide the student with:</li> <li>Distributed database concepts, architectures, and design prifor efficient data management in distributed environments.</li> <li>Query processing and optimization, transaction proconcurrency control, and DBMS reliability in distributed environments.</li> <li>Spatial data management, emphasizing spatial data types, in techniques, and their applications in database systems.</li> <li>Knowledge about the specialized database systems , XML information retrieval, and other databases, focusing on theo practical applications.</li> </ol>	nciples cessing, ributed ndexing -based ory and	
Course Outcomes:	Upon completion of the course, students will be able toECOMP725.1Design and implement distributed database systemECOMP725.2Explain the emerging Database Models, Techno and ApplicationsECOMP725.3Know how to design a Database and XML.ECOMP725.4Apply acquired knowledge for developing h solutions based on database systems/dat techniques.	logies olistic abase	
Charlenge & Dr. D	UNIT-1	$\mathcal{N}$	
	Distributed Database Concepts:Distributed Data processing, Distributed Database System, Promises of distributed database systems(DDBMS),Complicating factors,Problem Areas.DistributedDBMSArchitecture:DBMSStandardisation,Architectural models for distributed database systems,Distributed DBMS architecture.Distributed database database design: Alternative Design Strategies, Distributed Database design issues, fragmentation,allocation.	12 Hours	
Content:	UNIT-2		
content.	Overview Of Query processing: Query processing problem, Objectives of Query processing, characterization of query processors, layers of query processing.Optimization of Distributed Queries: Query OptimizationIntroduction to Transaction Processing: Definition of a transaction, 	11 Hours	

	measures, failures and fault tolerance in distributed systems, failures in distributed DBMS, Local reliability protocols and distributed reliability protocols.	
	UNIT -3	
	ParallelDatabasesystems:DatabaseServers,ParallelArchitectures,ParallelDBMStechniques,ParallelExecutionProblems.Problems.Problems.Problems.	
	Spatial Data management: Types of Spatial Data and Queries, Introduction to spatial indexes, Indexing based on space filling curves. Grid files, R-trees	11 Hours
	<b>Object Database Systems</b> : Motivating example, Structured Data types, Operations on structured data types, Encapsulation and ADTs, objects, alDs and reference types, OODBMS	
	UNIT -4	
	DeductiveDatabases:IntroductiontoRecursivequeries,TheoreticalFoundations,Recursivequerieswith	
	negation, from Datalog to SQL, Evaluating Recursive Queries Information Retrieval and XML data: Introduction to	
	Information retrieval and , Indexing for text search, web search engine, managing text in a DBMS, a data model for XML, Querying XML data, and efficient evaluation of XML queries. <b>Other Database Systems</b> : Multimedia databases,Mobile Databases,Geographical Information Systems,Temporal	11 Hours
Pedagogy:	Databases Inquiry based learning, Integrative, Reflective Learning, Construct	tive
Semence - Div		
	<ol> <li>Principles of Distributed Database Systems; 2nd Editied By M Ozsu and Patrick Valduriez, Person Education Asia.</li> <li>Database Management Systems, 3rd edition, Raghu Ramal and Johannes Gehrke, McGraw-Hill</li> </ol>	l. Tamer krishnan
References/	Reference book	
Readings:	1. Distributed Database; Principles & Systems By Publications,	Stefano
	<ol> <li>Ceri and Giuseppo Pelagatti,, McGraw-Hill International Edition</li> <li>Fundamentals of Database Systems, 6thEdition, Elmasri and N Addison. Wesley (2003).</li> </ol>	ns Iavathe,
	<ol> <li>Database System Concepts, 5th edition, Avi Silberschatz , H Korth , S. Sudarshan: McGraw-Hill (2010)</li> </ol>	Henry F.

Name of the Prog	gramme : Elect	tronics and Computer Engineering
Course Code	: ECO	MP730
Title of the Cours	e : Indu	strial Automation and Instrumentation Lab
Number of Credit	:s : 01	
Effective from AY	: 2024	I-25
Pre-requisites	Basic Electronics	
for the Course:		CINV2
Course Objectives:	<ol> <li>The subject aims</li> <li>An understand</li> <li>An ability to optimize transducers.</li> <li>An ability to optimize transducers.</li> <li>An ability to optimize transducers.</li> </ol>	to provide the student with: ading of the working of the CRO trainer. determine the characteristics of the different types of the construct the virtual instruments using the LABVIEW. o develop PLC Ladder diagrams for industrial control
	Upon completion	of the course, students will be able to
	ECOMP730.1	Demonstrate the working of the CRO trainer.
Course	ECOMP730.2	Determine the characteristics of the different types of the transducers.
Outcomes:	ECOMP730.3	Construct the virtual instruments using the LABVIEW.
	ECOMP730.4	Develop PLC Ladder diagrams for industrial control mechanisms.
	Following experi	ments should be conducted.
Content:	<ol> <li>Fault simulati</li> <li>Virtual Instruit</li> <li>Loops and St</li> <li>Arrays and Clips</li> </ol>	on using CRO trainer ments , Sub VIS ructures using LABVIEW usters using LABVIEW
	5. Implementati	ons Using Transducers
	6. Data Acquisit	ion using LABVIEW
	7. Ladder progra	am to implement fatching, interlocking, jogging
	8. Ladder progra	am to implement timing & counting applications.
	s. implement a	iny of the above lauder programs using the SCADA
	Inquiry based	learning Constructive planning of experiments
Pedagogy:	,Collaborative ap	proach in performing experiments
References/ Readings:	<ol> <li>H. S. Kalsi; Ele</li> <li>Robert H. Bis Education, 20</li> <li>John Webb, F &amp; Application</li> </ol>	ectronic Instrumentation; Third edition,Tata McGraw Hill. hop; Learning with LABVIEW 7 Express; Pearson 203. Ronal Weiss; Programmable Logic Controllers: Principles as, 5th Edition; Prentice Hall of India

<u>SEMESTER – VIII</u>		
Name of the Pro	gramme : Electronics and Computer Engineering	
Course Code	: ECOMP810	
Title of the Cour	se : Cryptography and Network Security	
Number of Cred	its : 3	
Effective from A	Y : 2024-25	
Pre-	Applied Mathematics	
requisites for		
the Course:		
Course Objectives:	<ol> <li>An understanding of the fundamentals of cryptography.</li> <li>Knowledge about the various encryption techniques.</li> <li>An understanding of the concept of public key cryptosystems.</li> <li>Ability to learn about message authentication and hash functions</li> <li>Ability to impart knowledge on network security.</li> </ol>	
	Upon completion of the course, students will be able to	
Course	ECOMPDescribe the fundamentals of a secure network and anal various encryption techniques in modern cryptography.ECOMPIllustrate various Public Key cryptographic techniques.810.20.2	lyse
Outcomes:	ECOMP Discuss various message authentication code, cryptograp 810.3 hash function and digital signatures.	ohic
	ECOMP Explain and Identify security aspects of various layers. 810.4	
	UNIT-1	AS CA
The Fair and the f	Introduction: Introduction to Cryptography, Security Threats, Vulnerability, Active and passive attacks, security service and Mechanisms, Conventional Encryption Model, CIA model. Basics of Cryptography: Plain Text and Cipher Text, Symmetric cipher Model, Cryptography, Cryptanalysis, Brute force Attacks.	10 hours
	UNIT-2	
Content	<ul> <li>Encryption Substitution Techniques: Caesar Cipher, Mono- alphabetic cipher, Poly-Alphabetic Cipher, Playfair Cipher,Hill Cipher,One time pad and Vigenere Cipher.</li> <li>Transposition Techniques: Rail fence techniques, Rotor Machine and Steganography.</li> <li>Block ciphers and Data Encryption Standards: Stream cipher, Block ciphers, Fiestal Cipher, Simplified Data Encryption standard and Data encryption standard</li> </ul>	10 hours
	UNIT-3	
	<ul> <li>Public key cryptography and RSA: Principles of public key cryptosystems–Public key Cryptosystems–application for public key cryptosystem, RSA algorithm, Deffie Helman key exchange algorithm.</li> <li>Cryptographic Hash Functions–Applications of cryptographic Hash</li> </ul>	13 hours

	<ul> <li>functions, 2 simple Hash functions, requirement and Security.</li> <li>Message Authentication Codes: Message Authentication</li> <li>Requirements, Message Authentication Functions.</li> <li>Digital Signature: Digital Signature, Properties and Digital signature</li> <li>Requirements, Digital Signature Standard.</li> </ul>	
	UNIT-4	
	<b>Firewalls:</b> Definition, Firewall Characteristics, Types of Firewalls. <b>509</b> Authentication Services: X.509 Definition, X.509 Certificate. <b>Authentication Applications:</b> Kerberos version 5. <b>Electronic Mail Security:</b> PGP (Transmission, Reception, General format of PGP), S/ MIME functions.	12 hours
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constr learning and Collaborative learning	uctive
References/ Readings:	<ol> <li>Textbooks:         <ol> <li>William Stallings, Cryptography and Network Security, 4th energy Prentice Hall of India, 2008.</li> <li>Cryptography and Network Security, Behrouz A. Forouzan, Demukhopadyay, McGraw Hill Education, 2nd Edition, 2010.</li> </ol> </li> <li>References:         <ol> <li>Cryptography and Network Security, Atul Kahate, McGraw Education, 3rd Edition, 2011.</li> <li>C. Kaufman, R. Perlman, and M. Speciner, Network Security: P Communication in a Public World, 2nd edition, Pearson Edu (Asia) Pvt. Ltd., 2002.</li> <li>William Stallings, "Network Security Essentials Applications Standards", 2nd ed., Pearson Education, 2003.</li> </ol></li></ol>	dition, bdeep v Hill private cation s and





: Electronics and Computer Engineering
: ECOMP821
: Compiler Design
: 3
: 2024-25

	Small	
Pre-requisites	Discrete Mathematics — 😵 🖗	
for the Course:	b set of b	
Course Objectives:	<ol> <li>To understand the basic principles of compiler design, its constituent parts, algorithms and structures required to be u the compiler.</li> <li>To understand the need to follow the syntax in writi application program and to learn how the analysis phase of co is designed to understand the programmer 's requirements v ambiguity</li> <li>To understand the relationship between machine and ass language, compilers, interpreters, linkers, loaders, assemblers</li> <li>To study construction of symbol tables, code gene optimization techniques.</li> </ol>	various used in ing an ompiler vithout sembly ration,
Course Outcomes:	Upon completion of the course, students will be able to:ECOMPUnderstand fundamentals of compiler design and id821.1the relationships among different phases of the comECOMPIllustrate the role of parser in compiler design.821.2ECOMPECOMPUnderstand tools to automate compiler construction821.3as LEX and YACCECOMPApply the concepts to design, implement, and test a compiler for a simple language.	entify piler.
Content	UNIT-1	
	Language processor concepts. Structure of a compiler. <b>The Evolution of Programming Languages:</b> The move to higher level languages, Impacts on Compilers. <b>A Simple Syntax-Directed Translator:</b> Syntax Definition, Syntax- Directed Translation, Parsing, Lexical Analysis, Symbol Tables, Intermediate Code Generation. <b>Lexical Analysis:</b> The Role of the Lexical Analyzer, Specification of Tokens, Recognition of Tokens, Finite Automata. A Language for specifying lexical analyzer. Study of the features and applications of LEX/FLEX tool.	10 hours
	UNIT-2	

	<ul> <li>Syntax Analysis: Overview of Context free grammars, Defining Context Free Grammar for If, Nested IF, For, While, Switch, Nested For, Nested While. Derivations and Parse trees, Ambiguity, Elimination of Left recursion, Left factoring.</li> <li>Top down parsing: Recursive descent parsing and Predictive parsers.</li> <li>Bottom up parsing: Shift-reduce parsers. Operator precedence parsers, LR parsers.</li> <li>Study of YACC Tool: Programming with YACC. Combining YACC and FLEX.</li> </ul>	10 hours
	UNIT-3	
	Intermediate Code Generation: Intermediate Language, Declarations, Assignment statements, Boolean expressions, Case statement, Backpatching, Procedure call. Run Time environments: Source language issues, Storage organization, Storage allocation. Error detection and recovery: Lexical phase errors, Syntactic phase errors, Semantic errors.	13 hours
	UNIT-4	2
Rent and the second sec	Symbol tables: The content of a symbol table, Data structures for Symbol Table Code generation: Issues in the design of a code Generator, Basic blocks and flow graphs, Next-use information, A simple Code generator, The DAG representation of Basic blocks, Peephole Optimization, Generating code from DAGS. Code optimization: The principle sources of optimization, Optimization of basic blocks, Machine dependent optimization, Register allocation optimization.	12 hours
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Const learning and Collaborative learning	ructive
References/ Readings:	<ol> <li>Textbooks:         <ol> <li>Aho and Ulman ; Principles of Compiler Design; Publisher: publishing House, ISBN: 81- 85015-61-9, Second Edition, 2002</li> <li>Aho, Ulman and Sethi; Compilers, Principles, techniques and Publisher:Pearson Education Inc, 1986,2006 ISBN: 0-201-1008</li> <li>Vinu V. Das ; Compiler design with FLEX and YACC; PHI public ISBN:978-81-203-3251-5.</li> </ol> </li> <li>References:         <ol> <li>Louden; Compiler Construction, Principles and Practice; G Publication, ISBN:0-534-93972-4.</li> <li>Systems Programming; D M Dhamdere, 2011 Tata McGra Education Private Limited.</li> </ol> </li> </ol>	Narosa d tools; 8-6. cation, algotia aw Hill

Name of the Prog	gramme : Electronics and Computer Engineering	
Course Code	: ECOMP822	
Title of the Cours	e : Digital VLSI	
Number of Credit	ts :3	
Effective from AY	: 2024-25	
Pre-requisites	MOS transistor working	
for the Course:	GIND	
Course Objectives:	<ol> <li>The subject aims to provide the student with:</li> <li>The capability to draw CMOS logic Structures.</li> <li>An understanding of clocking strategies in sequential CMOS circ</li> <li>Knowledge of test circuits required in the VLSI domain.</li> <li>An understanding of semiconductor memory circuits.</li> </ol>	cuits.
	Upon completion of the course, students will be able to	
	ECOMP822.1 Draw the CMOS Logic Structures.	
	ECOMP822.2 Analyze clocking strategies in sequential CMOS circ	cuits.
Course Outcomes:	ECOMP822.3 <b>Understand</b> the test circuits required in the domain.	VLSI
	ECOMP822.4 <b>Understand</b> the working of semiconductor me circuits.	emory
SINVES	UNIT-1	200
	CMOS Logic circuits: BiCMOS Logic Circuits: Introduction, Basic BiCMOS Circuits: Static Behavior, Switching Delay in BiCMOS Logic Circuits, BiCMOS Applications. Pseudo-nMOS Logic, Dynamic CMOS Logic, Clocked CMOS logic (C <sup>2</sup> MOS), CMOS Domino logic.	12 Hours.
A Faufant	UNIT-2	S.
Content:	Clocking Strategies: Clocked Systems, Latches and Registers, Single Phase clocking, Two phase clocking, two phase memory structures, Clock distribution. Clock and I/O circuits: : Introduction, ESD Protection, Input Circuits, Output Circuits and L(di/dt) Noise, On-Chip Clock Generation and Distribution, Latch-Up and Its Prevention.	11 Hours.
	UNIT -3	
	Design for Manufacturability: Introduction, Process Variations, Basic Concepts and Definitions, Design of Experiments and Performance Modeling Design for testability : Introduction, Fault testing, observability, controllability, automatic test pattern generation (ATPG). Built in Self-test, Boundary scan.	11 Hours.
	UNIT -4	
	Semiconductor Memories: Introduction, Dynamic Random Access Memory (DRAM), Static Random Access Memory (SRAM), Nonvolatile Memory, Flash Memory, Ferroelectric Random Access Memory (FRAM)	11 Hours.
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constructi learning and Collaborative learning	ve

	Text Books:		
	1. Sung-Mo Kang, Yusuf Leblebici, CMOS Digital Integrated Circuits,		
	Analysis and Design, McGraw Hill, Fourth Edition.		
References/	2. Neil H. E. Weste, Kamran Eshraghian, Principles of CMOS VLSI Design,		
Readings:	A System Perspective, Pearson Education Asia, Second Edition.		
	Reference book		
	1. Digital Integrated Circuits, A Design Perspective, Jan M. Rabaey,		
	Anantha Chandrakasan, Borivoje Nikolic, Pearson, Second Edition.		









Name of the Progr	amme : Electronics and Computer Engineering	
Course Code	: ECOMP823	
Title of the Course	: Biomedical Electronics & Instrumentation	
Number of Credits	: 3	
Effective from AY	: 2024-25	
Pre-requisites	Basics of Electronics	
for the Course:	ANNES	
Course Objectives:	<ol> <li>An introduction to human physiological system which is important with respect to electronic design considerations.</li> <li>The knowledge of the principles of operation and design biomedical electronics &amp; instruments.</li> <li>An understanding of medical diagnosis and therapy techniques</li> <li>An ability to solve electronic engineering problems related medical field.</li> </ol>	very gn of ed to
	Upon completion of the course, students will be able to	
	ECOMP 823.1 Describe the generation of bio-potentials and under the basic bio-signals and their characteristics	rstand
	ECOMP 823.2 Discuss electrodes and different bio-potential electrodes biochemical transducers used to acquire bio-signals	rodes,
Section And	ECOMP 823.3 Outline safety while designing and using biom equipment & also discuss about telemedicine techno	edical ology
Course Outcomes:	ECOMP 823.4 Explain different measuring and monitoring systems as ECG, EEG, EMG, blood pressure monitoring artificial life support systems	s such g and
The second second	ECOMP 823.5 Understand different medical assistive techr therapeutic equipment, clinical labo equipment/techniques	niques ratory
	ECOMP 823.6 Discuss different medical imaging systems visualization and analysis of medical images	for
	UNIT-1	
	<b>Cell and its structure:</b> Resting and Action Potential, Bioelectric Potentials: ECG, EEG EMG, Cardiovascular system.	
	Electrodes: basic electrode theory, Nernst equation, biopotential	11
	electrodes, biochemical transducers.	hours
	check bazards, loakage currents; testing instruments for checking	
Content	safety parameters of biomedical electronic equipment	
	Measuring and monitoring systems: FEC_ECC_ENC with black	
	diagrams Artifacts in his potential recordings	
	Paremakers: Pacing modes lead wires and electrodes	
	synchronous nacemaker: rate responsive nacing	12 hrs
	Defibrillator: AC and DC.	
	Clinical laboratory Measurements: Blood cell Counter: Types Of	

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Name of the Progr	amme : Electronics and Computer Engineering		
Course Code	: ECOMP824		
Title of the Course	: Internet of Things		
Number of Credits	: 3		
Effective from AY	: 2024-25		
Pre-requisites	Nil		
for the Course:	Conver .		
Course Objectives:	<ol> <li>To understand the basic components of an IoT system.</li> <li>To understand the technologies and current standards relating to each of the IOT layers.</li> <li>To understand the importance of interoperability in IoT, and the concepts of Cloud computing and Fog computing via examples.</li> <li>To appreciate and understand the appropriate use of various IoT technologies through real-life case studies and examples.</li> </ol>		
	Upon completion of the course, students will be able to		
	ECOMP 824.1 Explain the basic components of an IOT system. ECOMP 824.2 Demonstrate familiarity with communication protechnology and standards relating to each layer systems.	tocols, in IoT	
Course	ECOMP 824.3 Explain the concepts of Cloud Computing and	d Fog	
Outcomes:	Computing and their relevance to IoT.	25	
	ECOMP 824.4 Demonstrate an understanding of tools and technologies that would be suited for buildin deploying IoT-based solutions in various appli domains through examples and case studies.	d IoT g and ication	
States of the States	UNIT-1		
	<ul> <li>Emergence of IoT: Introduction, Evolution of IoT, Enabling IoT and the Complex. Interdependence of Technologies, IoT Networking Components.</li> <li>IoT Sensing and Actuation: Sensors, Sensing Types, Actuators, Actuator Types, Actuator Characteristics.</li> <li>IoT Levels and deployment templates: IoT Level-1, IoT Level-2, IoT Level-3, IoT Level-4, IoT Level-5, IoT Level-6.</li> </ul>	11 hours	
	UNIT-2		
Content	<ul> <li>IOT Processing Topologies and Types: Data Format, Importance of Processing in IoT, Processing Topologies, Processing Offloading.</li> <li>IOT Architecture Connectivity Technologies: Layered Architecture for IoT, Introduction to connectivity technologies and standards, IEEE 802.15.4, Zigbee, RFID, NFC, LORA, 6LoWPAN, Wi-Fi, Bluetooth.</li> <li>IOT Communication Technologies: Introduction to communication technologies, Infrastructure Protocols and Discovery Protocols, Introduction to Data Protocols - RPL, MQTT.</li> </ul>	11 hours	

	CoAP, REST and Web Sockets.		
	UNIT-3		
	<b>IOT Interoperability:</b> Introduction, Standards, Frameworks. <b>Cloud Computing:</b> Introduction, Virtualisation, Cloud Models, Cloud Implementation, Sensor-cloud: Sensors-as-a-service. <b>Fog Computing and its application</b> : Introduction, Role of Cloud and Fog in IoT, View of a fog computing architecture, Fog Computing in IOT, Selected applications of FOG Computing.	11 hours	
	UNIT-4		
	<ul> <li>IOT Case studies and future trends:</li> <li>Agricultural IoT- Components of an agricultural IoT, Advantages of IoT in agriculture, Case Studies - In-situ assessment of leaf area index using IoT-based agricultural system Smart irrigation management system.</li> <li>Vehicular IoT- Introduction, Components of vehicular IoT, Advantages of vehicular IoT, Crime assistance in a smart IoT transportation system.</li> <li>Healthcare IoT- Introduction, Components of healthcare IoT, Advantages and risks of healthcare IoT.</li> <li>Case Studies- AmbuSens system. Evolution of New IoT Paradigms, Challenges Associated with IoT, Emerging Pillars of IoT.</li> </ul>	12 hours	
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Const learning and Collaborative learning	ructive	
References/ Readings:	<ol> <li>Textbooks:         <ol> <li>Sudip Misra, Anandarup Mukherjee, Arijit Roy, Introduction Cambridge University Press, 2020</li> <li>Vijay Madisetti and Arshdeep Bahga, Internet of Things: A Har Approach, VPT edition1, 2014</li> <li>Pethuru Raj and Anupama C. Raman, "The Internet of T Enabling Technologies, Platforms, and Use Cases", CRC Press, 2</li> </ol> </li> <li>References:         <ol> <li>Nitesh Dhanjani, Abusing the Internet of Things, Publisher/O'Reilly Publisher, 2015.</li> <li>RMD Sundaram, Shriram K. Vasudevan, Abhishek S. Naga Internet of Things, John Wiley and Sons, 2019</li> <li>Cuno Pfister, Getting Started with the Internet of Things: A So Approach to Connecting Everything, Apress Publications, 1st E 2014.</li> <li>Massimo Banzi, Michael Shiloh, Getting Started with the Ar Maker Media Publishers/O'Reilly. 3rd edition. 2015.</li> </ol> </li> </ol>	to loT, nds-On Things: 2017. Shroff arajan, Shroff calable dition, duino,	

Name of the Progr	amme : Elec	tronics and Computer Engineering			
Course Code	: ECOMP825				
Title of the Course	: Data Analytics				
Number of Credits	: 3				
Effective from AY	: 2024	4-25			
Pre-requisites	Basics of progra	amming			
for the Course:		A INVE			
Course Objectives:	<ol> <li>To understand the data science fundamentals and process.</li> <li>To learn to manage data.</li> <li>To learn to describe the relationship between data using statistical methods.</li> <li>To present and interpret data R programming.</li> </ol>				
	Upon completion of the course, students will be able to				
Course	ECOMP 825.1	Describe the significance of data science and understand the Data Science process.			
	ECOMP 825.2	Explain how data is collected, managed, and stored for data science.			
outcomes.	ECOMP 825.3	Build, and prepare data for use with a variety of statistical methods and models.			
Seon UNIVERSITY	ECOMP 825.4	Perform data analytic & visualization using R programming.	P		
9 600 9	UNIT-1				
A Contraction of the second seco	Introduction To Data Science: Definition, Big Data and Data Science Hype, Datafication , Data Science Profile, Meta- Definition, Data Scientist, Statistical Inference, Populations and Samples, Populations and Samples of Big Data, Big Data Can Mean Big Assumptions, Modeling, Philosophy of Exploratory Data Analysis, The Data Science Process , A Data Scientist's Role in this Process Case Study: RealDirect.				
	UNIT-2				
Content	Data Munging: Properties of Data, Languages for Data Science, Collecting Data, Cleaning Data, Crowdsourcing.Mathematical Preliminaries: Probability, Descriptive Statistics, Correlation Analysis.Scores and Rankings: Developing Scoring Systems, Z-scores and Normalization, Advanced Ranking Techniques		11 hours		
	UNIT-3				
	Statistical Ana Distributions, S values. Visualizing Da Visualization Ac	alysis: Sampling from Distributions, Statistical statistical Significance, Permutation Tests and P- tatistical Significance Analysis, Developing a esthetic, Chart Types, Great Visualizations.	11 hours		
	UNIT-4				

	Introduction to R: GUI of R, R nuts and Bolts, Getting data into & out of R, Data types in R, Basic operations, Basic statistics, Generic functions, Data visualization using R, Data exploration & presentation, Statistics for model building & evaluation.11 hoursCase study using R: Call Data Record analytics, Medical Data AnalysisAnalysis11		
Pedagogy:	Inquiry based learning, Integrative, Reflective Learning, Constructive learning and Collaborative learning		
	Textbooks:		
	1. Steven S. Skiena, "The Data Science Design Manual", Springer 2017		
	Vijay Madisetti and Arshdeep Bahga, Internet of Things: A Hands-On		
	Approach, VPT edition1, 2014.		
	2. Rachel Schutt & O'neil, "Doing Data Science", Straight Talk from The		
	Frontline O'REILLY, ISBN:978-1-449-35865-5, 1st edition, October		
	2013.		
References/	3. Mark Gardner, "Beginning R: The Statistical Programming		
Readings:	Language", Wrox Press (WILEY), 2012		
	References:		
	1. Joel Grus," Data Science from Scratch" First Edition, April 2015.		
AND	2. Gareth James, Daniela Witten, Trevor Hatie, Roberst Tibhirani , "An		
	Introduction to Statistical Learning-with Applications in R <sup>#</sup> , 2013		
Smark	3. Jure Leskovek, Anand Bajaraman and Jeffrey Ullman, Mining of		
9 6 395 9	Massive Datasets v2.1 Cambridge University Press 2nd edition		
6 12 20 0	4. R Programming for Data Science, Roger D. Peng, LeanPub, 2015.		



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